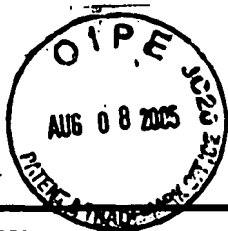


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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/714,465
				Filing Date	November 14, 2003
				First Named Inventor	Jinquan Dai
				Art Unit	2155 2192
				Examiner Name	Ben C. Wang
Sheet	2	of	2	Attorney Docket Number	42P17829

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T²
BW		INTEL, "Introduction to the Auto-Partitioning Programming Model" White Paper 'Online! October 2003 (2003-10), pp. 1-10, XP002333777		
BW		Rakhmatov, D. N. et al., "Hardware-Software Partitioning for Dynamically Reconfigurable Systems", Proc. of the 10th Int'l Symnp. on Hardware/Software Codesign Codes, May 2002, pp. 145-150, XP002333778		
BW		Liu, H., et al., "Network flow baesd circuit partitioning for time-multiplexed FPGAs", 1998 IEEE/ACM Int'l Conf. on Computer-Aided Design, NY, NY, 1998, pp. 497-504, XP002333779		
BW		Lakshmanamurthy, S., et al., "Network Processor Performance Analyis Methodology", Intel Tech. J., vol. 6, no. 3, 08/15/02, pp. 19-28, XP002333775		
BW		Dai, J., et al. "Automatically Partitioning Packet Processing Applications for Pipelined Architectures", Proc. of ACM Sigplan 2005, June 2005, pp. 237-247, XP002333780		

Examiner Signature	/Ben Wang/	Date Considered	05/31/2007
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

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[illegible]

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